

REMARKS

The Applicants request reconsideration of the rejection.

Claims 1-13 remain pending.

The Examiner objected to the drawings as failing to disclose all features claimed in independent Claim 1. The Applicants have reviewed Claim 1 in the absence of a specific indication as to which features are not shown in the drawings. Respectfully, the Applicants believe that the drawings disclose the features claimed in Claim 1 to the extent required by 37 CFR §1.84. For example, the power amplifier claimed in Claim 1 reads on the power amplifier 10 of Fig. 1. The claimed current detector reads on current detector 20 of Fig. 1 (Claim 1 has been amended to recite "detecting circuitry" which also reads on current detector 20). The claimed current-voltage converting means reads on current/voltage converter 40. The claimed error amplifier reads on amplifier 50. The bias generator is claimed broadly enough to read on circuitry providing an output V_{apc} as well as circuitry creating bias voltages applied to transistors TR1-TR3 (Fig. 5). The claimed current converter reads on square root converter 30 of Fig. 1. Thus, the Applicants request withdrawal of the objection. It is important to note that the examples of claimed structure reading on the illustration are

meant in no way to limit the scope of the claims, but are merely advanced as examples of the sufficiency of the drawings with respect to the claims.

If the Examiner finds that any specific elements of the claims are not shown in the drawings, the Applicants request the Examiner to provide specific examples so that consideration may be given to amending the claims or drawings.

The Examiner also objected to Claims 1, 9, and 11, for minor alleged informalities set forth on Page 3 of the Office Action. In reply, the Applicants note that claims 1, 9, and 11 have been amended to clarify the invention without narrowing their scope.

Claims 1 and 9 were rejected 35 U.S.C. §112, second paragraph, as set forth on Page 4 of the Office Action. The Applicants traverse as follows.

Regarding Claim 1, the Examiner finds that the phrase a function which has no local maximum and monotonously increases in an upward convex form lacks description. In reply, the Applicants note that this feature relates to and is described (without undue limitation) by the generation of an output current I_{sns} by the current detector 20, which is related with the output power V_{out} from power amplifier 10. Current I_{sout} is converted from the output current I_{sns} by square root

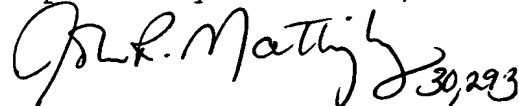
converter 30 or logarithm converter 30', and the conversion function (i.e., converting $I_{\text{sout}} - I_{\text{sns}}$) has no local maximum and monotonously increases in an upward convex form, as set forth on Page 33, lines 10-26 of the specification. See also Figs. 3 and 4, wherein the voltage V_{sns} is converted from the current I_{sout} by the current/voltage converter 40. Thus, the voltage V_{sns} is correlated closely with the output power V_{out} of the power amplifier.

Also regarding Claim 1, the Examiner asserted that it is not clear which part of the circuit is the "bias generator". In reply, a preferred embodiment of the invention generates bias voltages to power amplifiers TR1-TR3 from voltage V_{apc} provided by error amplifier 50 via resistors RP1-RP4. Although the bias generator is broader than this specific example, the Applicants believe that Claim 1 is definite in view of the specific example.

Finally, regarding Claim 9, the claim has been amended to refer non-specifically to a power amplifying transistor that is included in the power amplifier.

In view of the foregoing amendments and remarks, the Applicants believe that the application is in condition for allowance, and respectfully request the Examiner to pass the application to issue.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read "D.J. Stanger", followed by the handwritten number "30,293".

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